

Silverstone PCIe

1 B A

	12V	PRSENT1	Sense Pin - Card Presence Detect Direct trace to other sense pin
	12V	12V	
	12V	12V	
	G	G	
System Management Bus (SMB)	SMBCLK	JTAG	Connection Testing
System Management Bus (SMB)	SMBDAT	JTAG	Connection Testing (bridge these two)
	G	JTAG	Connection Testing (bridge these two)
	3.3V	JTAG	Connection Testing
Connection Testing	JTAG	3.3V	
	3.3V aux.	3.3V	
Signal from card to mobo (optional)	Wake	PWR Good	Power and REFCLK stabilised (message from mobo to card) (Direct trace to IC)

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Request running reference clock (signal from card to mobo)
Should be able to tie to ground permanently i.e. pulled low

	CLKREQ	G	
	G	REFCLK	Reference Clock differential pair - 100MHz +/- 300ppm Direct trace to IC
Differential Data Pair Direct trace to IC	Data Transmit	REFCLK	Reference Clock differential pair - 100MHz +/- 300ppm Direct trace to IC
Differential Data Pair Direct trace to IC	Data Transmit	G	
	G	Data Receive	Differential Data Pair Direct trace to IC with 0.1uF cap close to PCIe pin
Sense Pin - Card Presence Detect (direct trace to other sense pin)	PRSENT	Data Receive	Differential Data Pair Direct trace to IC with 0.1uF cap close to PCIe pin
	G	G	

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